RESPONSE UNDER 37 CFR 1.116

EXPEDITED PROCEDURE **EXAMINING GROUP 2183**

RECEIVED **CENTRAL FAX CENTER**

APR 1 4 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Simar et al

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman

Filed: October 31, 2000

Docket: TI-30556

For: MICROPROCESSOR WITH EXECUTION PACKET SPANNING TWO OR MORE

FETCH PACKETS

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 CERTIFICATION OF FAX TRANSMITTAL UNDER 37 C.F.R. \$1.6(b)

I hereby certify that the above correspondence is being facsimile transmitted to the Patent and Trademark Office on April 14, 2004.

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated February 19, 2004. amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows:

To:

Technology Center: 2183

Facsimile Number: 703-872-9306

Total Pages Sent: 11

From:

Robert D. Marshall, Jr.

Texas Instruments Incorporated

Facsimile: 972-917-4418 Phone: 972-917-5290 RECEIVED CENTRAL FAX CENTER

APR 1 4 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OFFICIAL

In re the Application of

Laurence R. Simar, Jr., et al.

Art Unit: 2183

TI-30559

Serial No.:

09/702,320

Examiner: David J. Huisman

Filed:

October 31, 2000

Conf. No.: 9784

For:

Microprocessor With Execution Packet Spanning Two or More Fetch Packets

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:

Ohn Barn

__April 14, 2004__

Robin E. Barnum

Date

FACSIMILE COVER SHEET

X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION (# Pages) ASSIGNMENT (# Pages) FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N (# Pages) DIVISIONAL APP'N		X AMENDMENT 116 (10 Pages) EOT (# Page) NOTICE OF APPEAL (# Pages) APPEAL (# Pages) ISSUE FEE (# Pages) REPLY BRIEF (IN TRIPLICATE) (# Pages)
Laurence R. Simar, Jr., et al. TITLE OF INVENTION: Microprocessor With Execution Packet Spanning Two or More Fetch Packets TI FILE NO.: DEPOSIT ACCT. NO.: TI-30559 PAXED: 4/14/04 DUE: 4/19/04		Serial No.: 09/702,320 Filing Date: October 31, 2000
ATTY/SECY:	RDM/reb	

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